AMENDMENTS TO THE CLAIMS

1.   (Currently Amended) A method of accessing PCI bus data via a debug card, comprising:

accessing to data of the PCI bus via a PCI interface of the debug card;

storing the data in a buffer of the debug card;

controlling the access to the data stored in the buffer of debug card by means of a data control chip of the debug card, the step of controlling the access to the data stored in the buffer of debug card comprising:

initializing the data control chip;

if the data control chip is in an idle status, setting the data control chip, wherein the setting step further comprises:

setting a data access mode of the data control chip;

determining a data access situation of the debug card and performing counting;

setting an amount of data to be accessed each time; and

ending the idle status; and

if the data control chip is not in the idle status, accessing to the PCI bus data stored in the debug card according to the settings of the data control chip;

storing the data in a buffer of the data control chip; and

extracting the data stored in the buffer of the data control chip via a host interface of the debug card.

2.   (Original) The method of claim 1, wherein extracting the data stored in the buffer of the data control chip via a host interface of the debug card further comprising:

transferring the data stored in the buffer of the data control chip to the host; and

analyzing the data stored in the host.

3.   (Cancelled)

4.   (Currently Amended) The method of ~~claim 3~~claim 1, wherein initializing the data control chip further comprising:

performing a synchronization setting of the data control chip and debug card;

setting an operating mode of the data control chip;

selecting a register address in the data control chip and writing an access control code therein;

setting a data access width of the data control chip; and

clearing the buffer of the debug card.

5.   (Cancelled)

6.   (Currently Amended) The method of ~~claim 5~~claim 1, wherein ending the idle status further comprising preparing to perform a next data access.

7.   (Currently Amended) The method of ~~claim 3~~claim 1, wherein if the data control chip is not in an idle status, accessing to the PCI bus data according to the settings of the data control chip means accessing to the PCI bus data according to a control code stored in a register of the data control chip.

8.   (Original) A debug card device, comprising:

a PCI interface operable as a connecting interface with a PCI bus;

a storage module for storing PCI bus data; and

a data control chip operable to control the access and transmission of PCI bus data, the data control chip including:

an access control module for controlling data access according to a control signal;

a transmission control module for controlling data transmission according to a control signal;

a data storage module for storing PCI bus data obtained from the debug card;

a register for storing an access control command; and

a host interface operable as an interface with a host.

9.   (Original) The debug card device of claim 8, further being connected to a host via a host interface, wherein the host comprising:

a host interface operable as a connecting interface with the debug card;

a data access module operable to control the access of the PCI bus data in the data control chip of the debug card;

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a driving module providing an access control firmware program; and

a data storage module for storing extracted PCI bus data.

10.   (Original) The debug card device of claim 8, wherein the storage module is a buffer of the debug card.

11.   (Original) The debug card device of claim 8, wherein the data storage module is a buffer of the data control chip.

12.   (Original) The debug card device of claim 8, wherein the host interface is a USB interface.

13.   (Original) The debug card device of claim 9, wherein the host interface is a USB interface.

14.   (New) The method of claim 1, wherein the data control chip is a chip of a model EZ-USBFX2.

15.   (New) The method of claim 1, wherein the determination of the data access situation of the debug card enables to evaluate whether the buffer of the debug card is full, which establishes a basis for calculating an accumulation of the data which means the amount of data accessed each time being accumulated into a data amount total, setting the amount of data

determines a number of data packets to access it at a next non-idle status, and once the idle status

ends, the control chip turns to the non-idle status to perform data accessing.